

MODULE	AREA	YEAR	SEMESTER	ECTS CREDITS	COURSE
Electronic Systems	Electronic Systems Complements	4 th	7 ^o	6	Elective
LECTURER			ADDRESS		
Prof. Andrés Roldán Aranda			Dpto. Electrónica y Tecnología de Computadores. Faculty of Sciences. Office nº 11. Telephone. 958-24-40-10. Email: amroldan@ugr.es		
			TUTORIAL ASSISTANCE		
Pablo Escobedo Araque			Wed/Thur/Fri. from 12:00-14:00. Email before reach the office		
			Dpto. Electrónica y Tecnología de Computadores. E.T.S. de Ingenierías Informática y de Telecomunicación. C/ Periodista Daniel Saucedo Aranda S/N C.P. 18071. Despacho D1.5 del CITIC Email: pabloescobedo@ugr.es; Office number: DB6; Phone: 958248845		
			TUTORIAL ASSISTANCE		
			Monday	10:00-12:00	D1.5 del Citic
			Wednesday	10:00-12:00	D1.5 del Citic
GRADE			OTHER MASTER		
Telecommunication Technology Engineering Grade			Telecommunication Engineering Master		
RECOMMENDATIONS					



UNIVERSIDAD
DE GRANADA

INFORMACIÓN SOBRE TITULACIONES DE LA UGR
grados.ugr.es

Firmado por: JUAN ANTONIO LOPEZ VILLANUEVA Director/a de Departamento

Sello de tiempo: 22/05/2019 19:51:40 Página: 1 / 8



9pJs076lgeoF0XSCrxI2pn5CKCJ3NmbA

La integridad de este documento se puede verificar en la dirección <https://sede.ugr.es/verifirma/pfinicio.jsp> introduciendo el código de verificación que aparece debajo del código de barras.

- Fluent reading of scientific English
- Knowledge of fundamental electronic design
- Basic knowledge of experimental techniques in the electronic lab
- Foundations of electromagnetic fields and transmission lines
- Basic computer skills

BRIEF DESCRIPTION OF CONTENTS

Printed circuit boards (PCB). CAD/CAM tools for the development of electronic circuits and products. Manufacture processes in the industry. Quality criteria and cost estimation. Electronic product design & development.

GENERAL AND SPECIFIC SKILLS

Skills training common to the industry Telecommunication:

C3 - Ability to use search tools for library resources or information related to telecommunications and electronics.

Specific skills module:

O6 - Ability to recognize and design printed circuit boards, to know tools, technologies and quality standards.

Transversal skills:

G1 - Capacity for analysis and synthesis: Find, analyze, critique (critical thinking), relate, structure and synthesize information from various sources and integrate ideas and knowledge.

G2 - Organization, planning and management of production information.

G3 - Oral and written communication in academic and professional fields with special emphasis on writing technical documentation.

G4 - Ability to solve problems.

G5 - Ability to make decisions based on objective criteria (experimental data or scientific simulation) and ability to argue or logically justify those decisions.

G6 - Ability to use and application of ICT in the academic and professional field.

G7 - Ability to communicate in a foreign language, particularly English.

G8 - Ability to work in team.

G9 - Capacity for independent learning and initiative and entrepreneurship

G10 - Motivation for quality and continuous improvement, acting with rigor, responsibility and ethics professional.

GOALS

The student should be able:

- To read and understand datasheets and articles from the main PCB technological papers.
- To make a public presentation of one among the themes of the course and be able to answer the questions emerged in the discussion.
- To relate mechanical and electronic results with product manufacturing procedures.
- Understand the concepts and nomenclature of the design and manufacturing soldering technology in micrometer and millimetre scale.
- Find the conditioning elements of the technology in order to make the PCB designs for general and specific applications.
- To choose the different electronic components in a circuit depending on the technique used for industrial



UNIVERSIDAD
DE GRANADA

INFORMACIÓN SOBRE TITULACIONES DE LA UGR
grados.ugr.es

Firmado por: JUAN ANTONIO LOPEZ VILLANUEVA Director/a de Departamento

Sello de tiempo: 22/05/2019 19:51:40 Página: 2 / 8



9pJs076lgeoF0XSCrxI2pn5CKCJ3NmbA

La integridad de este documento se puede verificar en la dirección <https://sede.ugr.es/verifirma/pfinicio.jsp> introduciendo el código de verificación que aparece debajo del código de barras.

manufacturing.

- Satisfy the standards and quality criteria in the design of printed circuits.
- Achieve introductory knowledge for different manufacturing technologies and industrial production PCBs.
- To know and manage CAD tools for obtaining lithography masks and chemical processes required during manufacturing.
- Learn and design easy CAD models of 3D printing oriented electronic products.
- Read into technical specifications of electronic equipment and write technical documentation.
- Apply the technological concepts needed to optimize production times depending on the available processes in the manufacturing line.
- Ability to apply mathematical concepts to optimize the costs of manufacture and assembly of printed circuit boards during industrialization in outside plant.

SYLLABUS

THEME CONTENT:

MODULE 1: Printed Circuit Boards

- UNIT 1. Historical Review
- UNIT 2. Component packages
- UNIT 3. Design Rules
- UNIT 4. Electrical and Mechanical Design

MODULE 2: Electronic Product Fabrication

- UNIT 5. PCB and Fabrication Technology
- UNIT 6. PCB Documentation

PRACTICAL AGENDA:

- Seminars

Multilayer PCB Design
Heat Dissipation
Exhibition of last course integrated projects

- Laboratory Practice

Integrated Project: A complex design of an electronic product using CAD/CAE/CAM tools, including design and fabrication of an electronic prototype from the cradle to the grave. Post-processing of PCBs, collection and management of documentation for Electronic Product manufacturing in a Web Site. Last course integrated project collection available [here](#).

REFERENCES

BASIC BIBLIOGRAPHY

- Jon Varteresian, Fabricating Printed Circuit Boards, Newnes, 2002



UNIVERSIDAD
DE GRANADA

INFORMACIÓN SOBRE TITULACIONES DE LA UGR
grados.ugr.es

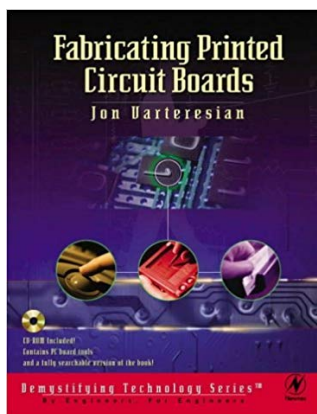
Firmado por: JUAN ANTONIO LOPEZ VILLANUEVA Director/a de Departamento

Sello de tiempo: 22/05/2019 19:51:40 Página: 3 / 8

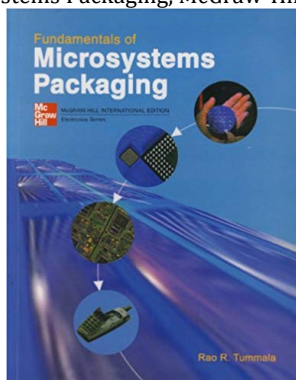


9pJs076lgeoF0XSCrxI2pn5CKCJ3NmbA

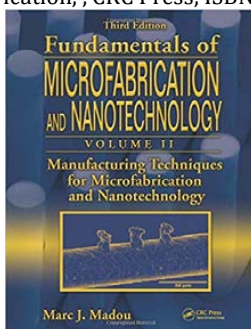
La integridad de este documento se puede verificar en la dirección <https://sede.ugr.es/verifirma/pfinicio.jsp> introduciendo el código de verificación que aparece debajo del código de barras.



- R. Tummala, Fundamentals of Microsystems Packaging, McGraw-Hill 2001



- Mark Madou, Fundamentals of Microfabrication, , CRC Press, ISBN: 0-8493-9451-1



- Elaine Rhodes, Developing Printed Circuit Assemblies: From Specifications to Mass Production, 2008



UNIVERSIDAD
DE GRANADA

INFORMACIÓN SOBRE TITULACIONES DE LA UGR
grados.ugr.es

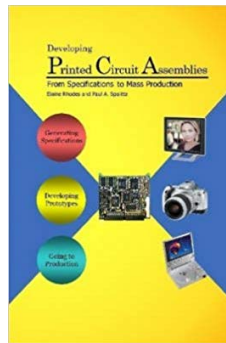
Firmado por: JUAN ANTONIO LOPEZ VILLANUEVA Director/a de Departamento

Sello de tiempo: 22/05/2019 19:51:40 Página: 4 / 8

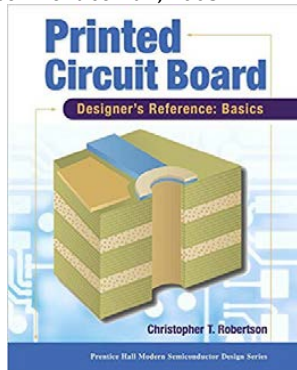


9pJs076lgeoF0XSCrxI2pn5CKCJ3NmbA

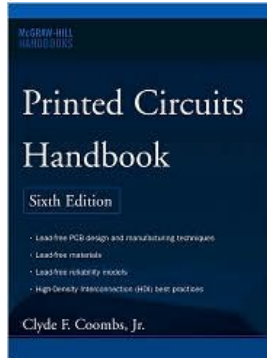
La integridad de este documento se puede verificar en la dirección <https://sede.ugr.es/verifirma/pfinicio.jsp> introduciendo el código de verificación que aparece debajo del código de barras.



- C. Robertson. PCB Designer's Reference. Prentice Hall, 2003



- C. Coombs, Printed Circuits Handbook, McGraw-Hill Professional, 6 edition, 2007



COMPLEMENTARY BIBLIOGRAPHY:

- V. Shukla, Signal Integrity for PCB Designers, Reference Designer, 2009



**UNIVERSIDAD
DE GRANADA**

INFORMACIÓN SOBRE TITULACIONES DE LA UGR
grados.ugr.es

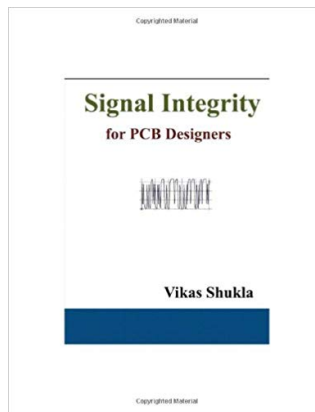
Firmado por: JUAN ANTONIO LOPEZ VILLANUEVA Director/a de Departamento

Sello de tiempo: 22/05/2019 19:51:40 Página: 5 / 8



9pJs076lgeoF0XSCrxI2pn5CKCJ3NmbA

La integridad de este documento se puede verificar en la dirección <https://sede.ugr.es/verifirma/pfinicio.jsp> introduciendo el código de verificación que aparece debajo del código de barras.



- [Engineering graphics with SolidWorks 2018 and video](#) instruction : a step-by-step project based approach, Planchard, David C., 2018. Biblioteca de la Fac. Ciencias: FCI/681.3 PLA eng



- D. Brooks, Signal Integrity Issues and Printed Circuit Board Design, Prentice Hall, 2003



- B. Archambeault, J. Dreuiawniak, PCB Design for Real-World EMI Control, Springer, 2002



UNIVERSIDAD
DE GRANADA

INFORMACIÓN SOBRE TITULACIONES DE LA UGR
grados.ugr.es

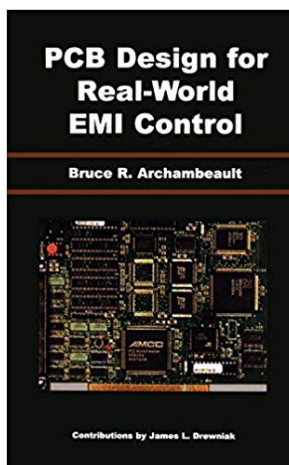
Firmado por: JUAN ANTONIO LOPEZ VILLANUEVA Director/a de Departamento

Sello de tiempo: 22/05/2019 19:51:40 Página: 6 / 8



9pJs076lgeoF0XSCrxI2pn5CKCJ3NmbA

La integridad de este documento se puede verificar en la dirección <https://sede.ugr.es/verifirma/pfinicio.jsp> introduciendo el código de verificación que aparece debajo del código de barras.



LINKS

http://electronica.ugr.es/~amroldan/asignaturas/curso19-20/printed_circuit_technology/contents/

WORKLOAD ESTIMATION

Modality	Names	Hours	ECTS	%
In-class work activities		60	2.4	40%
Theoretical classes	Theory	30	1.2	20%
Practical classes	Professional skills practice	22	0.88	15%
ECTS tutorials	Group tutorials	5	0.2	3%
Assessment	Assessment	3	0.12	2%
Distance education work activities		90	3.6	60%
Individual self-study	Progress assessment tasks	45	1.8	30%
Individual self-study	Working on theory contents	40	1.6	27%
Group self-study	Preparatory work for practical sessions	5	0.2	3%
Total		150	6	100%

Distance education work activities

Modality	Name	Description
Individual self-study	Progress assessment tasks	Essays and exercises will be assigned as homework to be assessed and corrected in class.
Individual self-study	Working on theory contents	Learning and revising theory and prototype. Completing assignments related to the theory items taught in class.
Group self-study	Preparatory work for practice sessions	Preparing the activities to be carried out before the class.

ATTENDANCE SYSTEM



UNIVERSIDAD
DE GRANADA

INFORMACIÓN SOBRE TITULACIONES DE LA UGR
grados.ugr.es

Firmado por: JUAN ANTONIO LOPEZ VILLANUEVA Director/a de Departamento

Sello de tiempo: 22/05/2019 19:51:40 Página: 7 / 8



9pJs076lgeoF0XSCrxI2pn5CKCJ3NmbA

La integridad de este documento se puede verificar en la dirección <https://sede.ugr.es/verifirma/pfinicio.jsp> introduciendo el código de verificación que aparece debajo del código de barras.

<p>Attendance at large group lectures is mandatory. Attendance at small group classes is mandatory. Attendance at seminars and realization of autonomous work will be mandatory for those students wishing this part to be assessed.</p>
<p>EVALUATION SYSTEM</p>
<p>In order to assess the acquisition of contents and skills, a diversified evaluation system will be used, selecting the most appropriate assessment technique for the subjects at all times, which allows to highlight the different knowledge and skills acquired by students when taking each course. One or more of the following evaluation techniques shall apply:</p> <ul style="list-style-type: none"> • <u>Theoretical part</u>: exams. Percentage of final qualification: 60%. • <u>Practical part</u>: labs activities, integrated project. Percentage of final qualification: 30%. • <u>Seminars</u> or <u>individual practical activities</u> will be evaluated considering the extra work carried out by students and advised by the professor. Percentage of final qualification: 10%. <p><i>To pass the course will be necessary to pass both the <u>theoretical, practical and seminar parts</u>.</i></p> <p>The grading system is expressed by numerical rating according to the provisions of art. 5 of R.D. 1125/2003 of 5 September, establishing the European credit system and grading system of official university degrees and valid national territory is established.</p>
<p>DETAILED INFORMATION ABOUT THE Final Single Assessment included in the "NORMATIVA DE EVALUACIÓN Y DE CALIFICACIÓN DE LOS ESTUDIANTES DE LA UNIVERSIDAD DE GRANADA"</p>
<p>For students qualifying for the final single assessment. This type of assessment will consist of all the evidence to prove that the student has acquired all of the general and specific skills described in the corresponding section of this Course Guide, including at least a theory and a practical laboratory production test. The final numerical grade will be obtained by the weighted sum of the ratings corresponding to a theoretical, the practical test.</p> <p>All matters relating to the assessment will be governed by the Student Evaluation and Qualification Policy at the University of Granada, which is available at this WEB URL. All matters relating to the assessment will be governed by the rules on teacher planning and organization of existing tests at the University of Granada.</p>
<p>ADDITIONAL INFORMATION</p>



UNIVERSIDAD
DE GRANADA

INFORMACIÓN SOBRE TITULACIONES DE LA UGR
grados.ugr.es

Firmado por: JUAN ANTONIO LOPEZ VILLANUEVA Director/a de Departamento

Sello de tiempo: 22/05/2019 19:51:40 Página: 8 / 8



9pJs076lgeoF0XSCrxI2pn5CKCJ3NmbA

La integridad de este documento se puede verificar en la dirección <https://sede.ugr.es/verifirma/pfinicio.jsp> introduciendo el código de verificación que aparece debajo del código de barras.